

V7P-AMD2

V7P Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill AMD 1000, 2000 2bills.

V7P dip-switch settings and functions:

| | FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | SW1 | SW2 | SW3 | SW4 |
|---|-----------------------------|---------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| ★ | Reject AMD 1000 | ON | | | | | | | | | | | |
| | Accept AMD 1000 | OFF | | | | | | | | | | | |
| ★ | Reject AMD 2000 | | ON | | | | | | | | | | |
| | Accept AMD 2000 | | OFF | | | | | | | | | | |
| | Reserved | | | ON | | | | | | | | | |
| ★ | Reserved | | | OFF | | | | | | | | | |
| | Reserved | | | | ON | | | | | | | | |
| ★ | Reserved | | | | OFF | | | | | | | | |
| | Reserved | | | | | ON | | | | | | | |
| ★ | Reserved | | | | | OFF | | | | | | | |
| | Reserved | | | | | | ON | | | | | | |
| ★ | Reserved | | | | | | OFF | | | | | | |
| | Harness Disable | | | | | | | ON | | | | | |
| ★ | Harness Enable | | | | | | | OFF | | | | | |
| | Inhibit Active High | | | | | | | | ON | | | | |
| ★ | Inhibit Active Low | | | | | | | | OFF | | | | |
| ★ | 1 pulse / AMD 1000 | | | | | | | | | OFF | OFF | | |
| | 2 pulses / AMD 1000 | | | | | | | | | ON | OFF | | |
| | 4 pulses / AMD 1000 | | | | | | | | | OFF | ON | | |
| | 20 pulses / AMD 1000 | | | | | | | | | ON | ON | | |
| ★ | Interface Timing Conversion | 50ms LO / 50ms HI | | | | | | | | | OFF | OFF | |
| | | 60ms LO / 300ms HI | | | | | | | | | ON | OFF | |
| | | 30ms LO / 50ms HI | | | | | | | | | OFF | ON | |
| | | 150ms LO / 150ms HI | | | | | | | | | ON | ON | |

★ Manufacture setting

◆ Note : (1) Default setting.

(2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

(3) Remove the front cover and the dip-switch in on the CPU board.

V7P-AMD2(Pulse/ICT104V/ICT104U/MDB)

Interface Settings: 2(Pulse) Currency Assign Data

| | INTERFACE | SW1 | SW2 | SW3 | SW4 |
|---|--------------------------|-----|-----|-----|-----|
| ★ | Credit-Pulse Normal HIGH | ON | | | |
| | Credit-Pulse Normal LOW | OFF | | | |
| ★ | Pulse Mode | | ON | OFF | |
| | ICT104U Mode | | OFF | OFF | |
| | ICT104V Mode | | OFF | ON | |
| | MDB Mode | | ON | ON | |
| ★ | Reserved | | | | ON |
| | | | | | OFF |

★ Manufacture setting

| Interface | ICT104U | ICT104V | Pulse | MDB |
|------------|----------|----------|----------|----------|
| Bill value | | | | |
| BV1 | AMD 1000 | AMD 1000 | AMD 1000 | AMD 1000 |
| BV2 | AMD 2000 | AMD 2000 | AMD 2000 | AMD 2000 |

Interface Settings: 2(ICT104U/ICT104V)

| | INTERFACE | SW1 | SW2 | SW3 | SW4 |
|---|--------------|-----|-----|-----|-----|
| ★ | Reserved | ON | | | |
| | Reserved | OFF | | | |
| | Pulse Mode | | ON | OFF | |
| ★ | ICT104U Mode | | OFF | OFF | |
| | ICT104V Mode | | OFF | ON | |
| | MDB Mode | | ON | ON | |
| ★ | Reserved | | | | ON |
| | | | | | OFF |

★ Manufacture setting

| | INTERFACE | SW1 | SW2 | SW3 | SW4 |
|---|--------------|-----|-----|-----|-----|
| ★ | Reserved | ON | | | |
| | Reserved | OFF | | | |
| | Pulse Mode | | ON | OFF | |
| | ICT104U Mode | | OFF | OFF | |
| ★ | ICT104V Mode | | OFF | ON | |
| | MDB Mode | | ON | ON | |
| ★ | Reserved | | | | ON |
| | | | | | OFF |

★ Manufacture setting

Interface Settings: 2(MDB)

| | FUNCTION | SW1 | SW2 | SW3 | SW4 |
|---|---|-----|-----|-----|-----|
| ★ | Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2 | ON | | | |
| | Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0 | OFF | | | |
| | Pulse Mode | | ON | OFF | |
| | ICT104U Mode | | OFF | OFF | |
| | ICT104V Mode | | OFF | ON | |
| ★ | MDB Mode | | ON | ON | |
| ★ | Reserved | | | | ON |
| | | | | | OFF |

★ Manufacture setting