

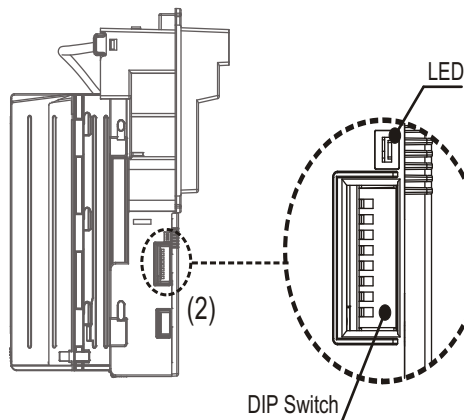
XBA-GEL6

XBA Option Switch Settings(Pulse/MDB/ICT104U/GPT/ICT104V)

Supported bill Lari 5, 10, 20, 50, 100, 200 6bills.

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject Lari 5 & 10	ON							
★ Accept Lari 5 & 10	OFF							
★ Reject Lari 20		ON						
★ Accept Lari 20		OFF						
★ Reject Lari 50			ON					
★ Accept Lari 50			OFF					
★ Reject Lari 100				ON				
★ Accept Lari 100				OFF				
★ Reject Lari 200					ON			
★ Accept Lari 200					OFF			
★ Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★ Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★ Stack Banknote when Power-up							ON	
★ Reject Banknote when Power-up							OFF	
★ Reserved								ON
★ Reserved								OFF

This dip switch is located at the side of XBA.



Currency Assign Data

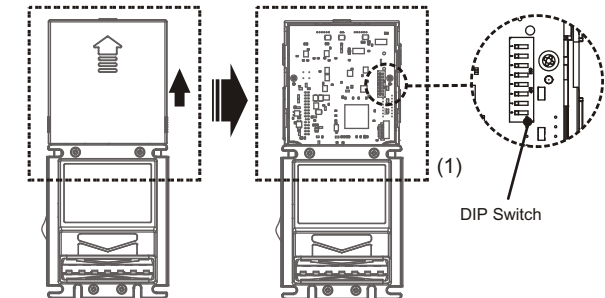
Interface Bill value	Pulse	MDB	ICT104V	ICT104U	GPT
BV1	Lari 5	Lari 5	Lari 5	Lari 5	Lari 5
BV2	Lari 10	Lari 10	Lari 10	Lari 10	Lari 10
BV3	Lari 20	Lari 20	Lari 20	Lari 20	Lari 20
BV4	Lari 50	Lari 50	Lari 50	Lari 50	Lari 50
BV5	Lari 100	Lari 100	Lari 100	Lari 100	
BV6	Lari 200	Lari 200	Lari 200	Lari 200	

Appendix

XBA-GEL6 (Pulse/MDB/ICT104U/GPT/ICT104V)

FUNCTION					SW1	SW2	SW3	SW4	SW5		SW6	SW7	SW8	
★	Inhibit Active HIGH				ON									
	Inhibit Active LOW				OFF									
★	Reserved					ON								
	Reserved					OFF								
★	Reserved						ON							
	Reserved						OFF							
★	Reserved							ON						
	Reserved							OFF						
★	Reserved							ON						
	Reserved							OFF						
★	Pulse Mode				MDB Mode			ICT104V Mode		Other Mode				
	Credit-Pulse Normal HIGH				Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2			Connect with coin changer		Reserved		ON		
★	Credit-Pulse Normal LOW				Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0			Connect with ICT VCCS converter board		Reserved		OFF		
	Pulse Mode											ON		
★	Other Mode											OFF		
	Pulse Mode				Other Mode									
★	1 pulse / Lari 5				MDB Mode								OFF	OFF
	2 pulse / Lari 5				GPT Mode								OFF	ON
★	5 pulse / Lari 5				ICT104U Mode								ON	OFF
	20 pulse / Lari 5				ICT104V Mode								ON	ON

This dip switch is located on the CPU board, remove the CPU board cover first.



★ Manufacture setting

After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note: (1) Please reset the bill acceptor after set the dip switch.

(2) Dip switches 1 to 5 are only used for pulse protocol.

(3) No sleep function.

(4) Calibration card is needed.