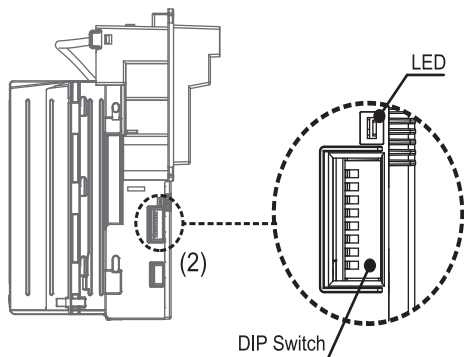


# XBA-OMR5 Option Switch Settings

Supported bill OMR 0.1, 0.5, 1, 5, 10 5bills.

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject OMR 0.1	ON							
★ Accept OMR 0.1	OFF							
★ Reject OMR 0.5		ON						
★ Accept OMR 0.5		OFF						
★ Reject OMR 1			ON					
★ Accept OMR 1			OFF					
★ Reject OMR 5				ON				
★ Accept OMR 5				OFF				
★ Reject OMR 10					ON			
★ Accept OMR 10					OFF			
★ Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★ Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★ Stack Banknote when Power-up							ON	
★ Reject Banknote when Power-up							OFF	
★ Reserved								ON
★ Reserved								OFF

This dip switch is located at the side of XBA.



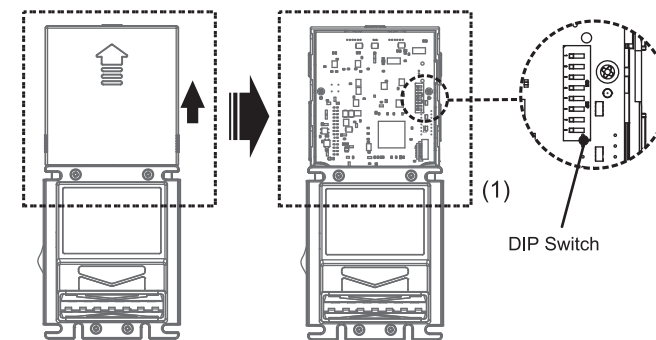
## Currency Assign Data

Interface Bill value	ICT104U	MDB	ICT104V	Pulse
BV1	OMR 0.1	OMR 0.1	OMR 0.1	OMR 0.1
BV2	OMR 0.5	OMR 0.5	OMR 0.5	OMR 0.5
BV3	OMR 1	OMR 1	OMR 1	OMR 1
BV4	OMR 5	OMR 5	OMR 5	OMR 5
BV5	OMR 10	OMR 10	OMR 10	OMR 10

# XBA-OMR5 (Pulse/MDB/ICT104U/ICT104V)

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	
Inhibit Active HIGH	ON								
★ Inhibit Active LOW	OFF								
★ Reserved		ON							
★ Reserved		OFF							
★ Reserved			ON						
★ Reserved			OFF						
★ Reserved				ON					
★ Reserved				OFF					
	<b>Pulse Mode</b>		<b>MDB Mode</b>		<b>ICT104V Mode</b>				
★ Credit-Pulse Normal HIGH	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 3		Connect with coin changer		ON				
★ Credit-Pulse Normal LOW	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 1		Connect with ICT VCCS converter board		OFF				
★ Pulse Mode						ON			
★ Other Mode						OFF			
	<b>Pulse Mode</b>		<b>Other Mode</b>						
★ 1 pulse / OMR 0.1			MDB Mode					OFF	OFF
★ 5 pulses / OMR 0.1			Reserved					OFF	ON
★ 10 pulses / OMR 0.1			ICT104U Mode					ON	OFF
★ 25 pulses / OMR 0.1			ICT104V Mode					ON	ON

This dip switch is located on the CPU board, remove the CPU board cover first.



### ★ Manufacture setting

After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note : 1.Please reset the bill acceptor after set the dip switch.

2.Dip switches 1 to 5 are only used for pulse protocol.

3.Calibrate card is needed.