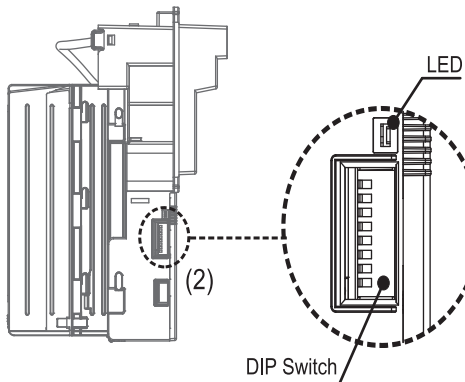# XBA-UAH9 Option Switch Settings

## Supported bill　H　1, 2, 5, 10, 20, 50, 100, 200, 500　9bills.

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|---|---|---|---|---|---|---|---|---|
| Reject　H　1 & 　2 | ON | | | | | | | |
| ★ Accept　H　1 & 　2 | OFF | | | | | | | |
| Reject　H　5 & 　10 | | ON | | | | | | |
| ★ Accept　H　5 & 　10 | | OFF | | | | | | |
| Reject　H　20 & 　50 | | | ON | | | | | |
| ★ Accept　H　20 & 　50 | | | OFF | | | | | |
| Reject　H　100 & 200 | | | | ON | | | | |
| ★ Accept　H　100 & 200 | | | | OFF | | | | |
| Reject　H　500 | | | | | ON | | | |
| ★ Accept　H　500 | | | | | OFF | | | |
| Disable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | ON | | |
| ★ Enable Bill Reject 4 Times BA Stop By 30 Sec | | | | | | OFF | | |
| Stack Banknote when Power-up | | | | | | | ON | |
| ★ Reject Banknote when Power-up | | | | | | | OFF | |
| Reserved | | | | | | | | ON |
| ★ Reserved | | | | | | | | OFF |

This dip switch is located at the side of XBA.



(2)　DIP Switch　LED

## Currency Assign Data

| Interface / Bill value | Pulse (When Dip 1 is ON) | Pulse (When Dip 1 is OFF) | MDB (When Dip 1 is ON) | MDB (When Dip 1 is OFF) | ccNet | RS232 A0 | ICT104U |
|---|---|---|---|---|---|---|---|
| BV1 | | H 1 | H 5 | H 1 | H 1 | H 1 | H 1 |
| BV2 | | H 2 | H 10 | H 2 | H 2 | H 2 | H 2 |
| BV3 | H 5 | H 5 | H 20 | H 5 | H 5 | H 5 | H 5 |
| BV4 | H 10 | H 10 | H 50 | H 10 | H 10 | H 10 | H 10 |
| BV5 | H 20 | H 20 | H 100 | H 20 | H 20 | H 20 | H 20 |
| BV6 | H 50 | H 50 | H 200 | H 50 | H 50 | H 50 | H 50 |
| BV7 | H 100 | H 100 | H 500 | H 100 | H 100 | H 100 | H 100 |
| BV8 | H 200 | H 200 | | H 200 | H 200 | H 200 | H 200 |
| BV9 | H 500 | H 500 | | H 500 | H 500 | H 500 | H 500 |

### RS232 A0 INFORMATION

**BillType Data**

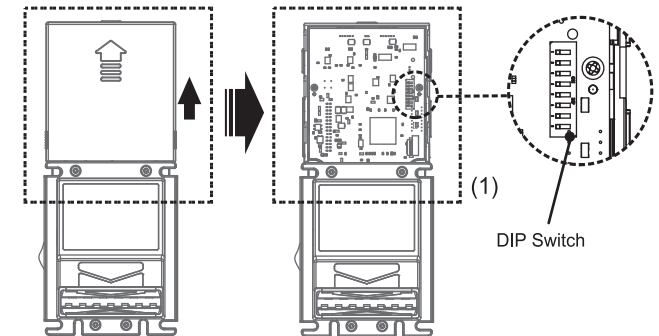| Code | Denomination |
|---|---|
| 1 | H 1 |
| 2 | H 2 |
| 3 | H 5 |
| 4 | H 10 |
| 5 | H 20 |
| 6 | H 50 |
| 7 | H 100 |
| 8 | H 200 |
| 9 | H 500 |

*Appendix*

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# XBA-UAH9 (Pulse/MDB/RS232 A0/ccNet/ICT104U)

| FUNCTION | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 |
|---|---|---|---|---|---|---|---|---|
| Inhibit Active HIGH | ON | | | | | | | |
| ★ Inhibit Active LOW | OFF | | | | | | | |
| Reserved | | ON | | | | | | |
| ★ Reserved | | OFF | | | | | | |
| Reserved | | | ON | | | | | |
| ★ Reserved | | | OFF | | | | | |
| Reserved | | | | ON | | | | |
| ★ Reserved | | | | OFF | | | | |
| **Pulse Mode** / **MDB Mode** / **ccNet Mode** | | | | | | | | |
| ★ Credit-Pulse Normal HIGH — Scaling Factor (SF) = 100, Decimal Point Position (DPP) = 2 / 9600 Baud Rate | | | | | ON | | | |
| Credit-Pulse Normal LOW — Scaling Factor (SF) = 1, Decimal Point Position (DPP) = 0 / 19200 Baud Rate | | | | | OFF | | | |
| Pulse Mode | | | | | | ON | | |
| ★ Other Mode | | | | | | OFF | | |
| ★ 1 pulse / H1 (Dip1:OFF), H5 (Dip1:ON) — MDB Mode | | | | | | | OFF | OFF |
| 5 pulses / H1 (Dip1:OFF), H5 (Dip1:ON) — RS232 A0 Mode | | | | | | | OFF | ON |
| 10 pulses / H1 (Dip1:OFF), H5 (Dip1:ON) — ccNet Mode | | | | | | | ON | OFF |
| 25 pulses / H1 (Dip1:OFF), H5 (Dip1:ON) — ICT104U | | | | | | | ON | ON |

★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.



(1)　DIP Switch

**After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note : 1. No sleep function.
　　　　2. Calibration card is needed.