

V7-TRY5

V7 Option Switch Settings: 1 (Pulse/MDB/ICT004U)

Supported bill TRY 5, 10, 20, 50, 100 5bills.

V7 dip-switch settings and functions:

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject TRY 5	ON							
★ Accept TRY 5	OFF							
★ Reject TRY 10		ON						
★ Accept TRY 10		OFF						
★ Reject TRY 20			ON					
★ Accept TRY 20			OFF					
★ Reject TRY 50				ON				
★ Accept TRY 50				OFF				
★ Reject TRY 100					ON			
★ Accept TRY 100					OFF			
★ Reserved						ON		
★ Reserved						OFF		
★ Harness Disable							ON	
★ Harness Enable							OFF	
★ Inhibit Active High								ON
★ Inhibit Active Low								OFF

◆ Factory configuration has all switches in the OFF position.

V7 Interface Settings: 2 (Pulse)

INTERFACE	SW1	SW2	SW3	SW4
★ 1 pulse / TRY 5	OFF	OFF		
5 pulse / TRY 5	ON	OFF		
10 pulse / TRY 5	OFF	ON		
20 pulse / TRY 5	ON	ON		
★ 50ms on / 50ms off			OFF	OFF
60ms on / 300ms off			ON	OFF
30ms on / 50ms off			OFF	ON
150ms on / 150ms off			ON	ON

★ Manufacture setting

V7-TRY5(Pulse/MDB/ICT004U)

V7 Interface Settings: 3 (ICT)

INTERFACE	SW1	SW2	SW3	SW4
★ Reserved	ON			
	OFF			
Pulse Mode		ON	OFF	
MDB Interface		OFF	OFF	
★ ICT Interface		OFF	ON	
Reserved		ON	ON	
★ Reserved				ON
				OFF

V7 Interface Settings: 3 (Pulse)

INTERFACE	SW1	SW2	SW3	SW4
★ Credit-Pulse Normal High	ON			
Credit-Pulse Normal Low	OFF			
★ Pulse Mode		ON	OFF	
MDB Interface		OFF	OFF	
ICT Interface		OFF	ON	
Reserved		ON	ON	
★ Reserved				ON
				OFF

V7 Interface Settings: 3 (MDB)

INTERFACE	SW1	SW2	SW3	SW4
★ Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
Pulse Mode		ON	OFF	
★ MDB Interface		OFF	OFF	
ICT Interface		OFF	ON	
Reserved		ON	ON	
★ Reserved				ON
				OFF

Currency Assign Data

Interface Bill value	ICT	Pulse	MDB
BV1	TRY 5	TRY 5	TRY 5
BV2	TRY 10	TRY 10	TRY 10
BV3	TRY 20	TRY 20	TRY 20
BV4	TRY 50	TRY 50	TRY 50
BV5	TRY 100	TRY 100	TRY 100

Note: 1. Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
2. Remove the front cover and the dip-switch in on the CPU board.
(Please refer to "Sw1" setting.)