

V7E+/LX7-AMD2

V7E+/LX7 Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill AMD 1000, 2000 2bills.

V7E+/LX7 dip-switch settings and functions:

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
★	Reject AMD 1000	ON											
★	Accept AMD 1000	OFF											
★	Reject AMD 2000		ON										
★	Accept AMD 2000		OFF										
★	Reserved			ON									
★	Reserved			OFF									
★	Reserved				ON								
★	Reserved				OFF								
★	Reserved					ON							
★	Reserved					OFF							
★	Reserved						ON						
★	Reserved						OFF						
	Pulse Mode	Other Mode											
★	Harness Disable	BA stops working when fishing						ON					
★	Harness Enable	Normal						OFF					
★	Inhibit Active High								ON				
★	Inhibit Active Low								OFF				
★	1 pulse / AMD 1000								OFF	OFF			
	2 pulses / AMD 1000								ON	OFF			
	4 pulses / AMD 1000								OFF	ON			
	20 pulses / AMD 1000								ON	ON			
★	Pulse Speed	50ms LO / 50ms HI									OFF	OFF	
		60ms LO / 300ms HI									ON	OFF	
		30ms LO / 50ms HI									OFF	ON	
		150ms LO / 150ms HI									ON	ON	

★ Manufacture setting

Note : 1. Default setting

2. Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

3. Remove the front cover and the dip-switch in on the CPU board.

V7E+/LX7-AMD2(Pulse/ICT104V/ICT104U/MDB)

DIP SWITCHES SETTING : 2(Pulse)

	FUNCTION	SW1	SW2	SW3	SW4
★	Pulse Normal High	ON			
	Pulse Normal Low	OFF			
★	Pulse Mode		ON	OFF	
	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
★	Reserved				ON
					OFF

Currency Assign Data

Interface Bill value	ICT104U	Pulse	MDB	ICT104V
BV1	AMD 1000	AMD 1000	AMD 1000	AMD 1000
BV2	AMD 2000	AMD 2000	AMD 2000	AMD 2000

DIP SWITCHES SETTING : 2(ICT104V)

	FUNCTION	SW1	SW2	SW3	SW4
★	Reserved	ON			
		OFF			
	Pulse Mode		ON	OFF	
★	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
★	Reserved				ON
					OFF

DIP SWITCHES SETTING : 2(ICT104U)

	FUNCTION	SW1	SW2	SW3	SW4
★	Reserved	ON			
		OFF			
	Pulse Mode		ON	OFF	
	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
★	ICT104U Interface		OFF	OFF	
★	Reserved				ON
					OFF

DIP SWITCHES SETTING : 2(MDB)

	FUNCTION	SW1	SW2	SW3	SW4
★	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
	Pulse Mode		ON	OFF	
★	ICT104V Interface		OFF	ON	
	MDB Interface		ON	ON	
	ICT104U Interface		OFF	OFF	
★	Reserved				ON
					OFF

★ Manufacture setting

Note 1: Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

2: Remove the front cover and the dip-switch in on the CPU board.(Please refer to "Sw1" setting.)