#### V7E/LX7-PEN5

## V7E/LX7 Option Switch Settings: 1(Pulse/ICT104V/MDB/ICT104U) Supported bill PEN 10, 20, 50, 100, 200 5bills.

#### V7E/LX7 dip-switch settings and functions:

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4
	Reject PEN 10	ON											
*	Accept PEN 10	OFF											
	Reject PEN 20		ON										
*	Accept PEN 20		OFF										
	Reject PEN 50			ON									
*	Accept PEN 50			OFF									
	Reject PEN 100				ON								
*	Accept PEN 100				OFF								
	Reject PEN 200					ON							
*	Accept PEN 200					OFF							
	Reserved						ON						
*	Reserved	OFF											
	Pulse Mode	Other Mode											
	Harness Disable	BA stops working when fishing ON											
*	Harness Enable			Nor	mal			OFF					
	Inhibit Active High								ON				
*	Inhibit Active Low	OFF											
*	1 pulse / PEN 10									OFF	OFF		
	2 pulses / PEN 10					ON	OFF						
	5 pulses / PEN 10									OFF	ON		
	20 pulses / PEN 10									ON	ON		
*		50	ms Lo	O / 5	0ms H	I						OFF	OFF
	Pulse Speed	60ms LO / 300ms HI						ON	OFF				
	i uise opeeu	30	ms Lo	) / 5	0ms H							OFF	ON
		150	ms L0	) / 150	Oms H	ı						ON	ON

#### ★ Manufacture setting

Note: (1) Default setting.

- (2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
- (3) Remove the front cover and the dip-switch in on the CPU board.

# Appendix

## V7E/LX7-PEN5(Pulse/ICT104V/MDB/ICT104U)

## DIP SWITCHES SETTING: 2(Pulse) Currency Assign Data

ı	FUNCTION	SW1	SW2	SW3	SW4
	Pulse Normal High	ON			
ı	Pulse Normal Low				
٠	Pulse Mode		ON	OFF	
ı	ICT104U Interface		OFF	ON	
ı	ICT104V Interface		ON	ON	
ı	MDB Interface		OFF	OFF	
ı	Reserved				ON
٠	reserved				OFF

Interface Bill value	ICT104U	Pulse	MDB	ICT104V	
BV1	PEN 10	PEN 10	PEN 10	PEN 10	
BV2	PEN 20	PEN 20	PEN 20	PEN 20	
BV3	PEN 50	PEN 50	PEN 50	PEN 50	
BV4	PEN 100	PEN 100	PEN 100	PEN 100	
BV5	PEN 200	PEN 200	PEN 200	PEN 200	

### **DIP SWITCHES SETTING: 2(ICT104V)**

*	FUNCTION	SW1	SW2	SW3	SW4
	Reserved	ON			
	Reserveu	OFF			
	Pulse Mode		ON	OFF	
*	ICT104U Interface		OFF	ON	
	ICT104V Interface		ON	ON	
	MDB Interface		OFF	OFF	
*	Reserved				ON
	Neserved				OFF

#### **DIP SWITCHES SETTING: 2(ICT104U)**

	FUNCTION	SW1	SW2	SW3	SW4
*	Reserved	ON			
	Reserved	OFF			
	Pulse Mode		ON	OFF	
*	ICT104U Interface		OFF	ON	
	ICT104V Interface		ON	ON	
	MDB Interface		OFF	OFF	
	Reserved				ON
*	Neserveu				OFF

## **DIP SWITCHES SETTING: 2(MDB)**

	FUNCTION	SW1	SW2	SW3	SW4
*	Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
	Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0				
	Pulse Mode		ON	OFF	
	ICT104U Interface		OFF	ON	
	ICT104V Interface		ON	ON	
*	MDB Interface		OFF	OFF	
	Reserved				ON
*					

★ Manufacture setting

Note 1: Default setting.

- 2: Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.
- 3: Remove the front cover and the dip-switch in on the CPU board.

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