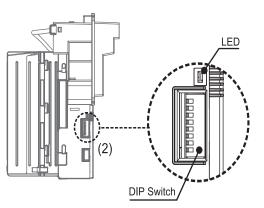
XBA-AED6 Option Switch Settings

Supported bill AED 5, 10, 20, 50, 100, 200 6bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject AED 5 & 10	ON							
*	Accept AED 5 & 10	OFF							
	Reject AED 20		ON						
*	Accept AED 20		OFF						
	Reject AED 50			ON					
*	Accept AED 50			OFF					
	Reject AED 100				ON				
*	Accept AED 100				OFF				
	Reject AED 200					ON			
*	Accept AED 200					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up							ON	
*	Reject Banknote when Power-up							OFF	
	Reserved								ON
*	Reserved	Reserved							

This dip switch is located at the side of XBA.



Currency Assign Data

Interface Bill value	ICT104U		JPSTD		ccTa l k		Pulse		RS232 A0	
BV1	AED	5	AED	5	AED	5	AED	5	AED	5
BV2	AED	10			AED	10	AED	10	AED	10
BV3	AED	20			AED	20	AED	20	AED	20
BV4	AED	50			AED	50	AED	50	AED	50
BV5	AED	100			AED	100	AED	100	AED	100
BV6	AED	200			AED	200	AED	200	AED	200

RS232 A0 INFORMATION

TROZOZ / TO ITTE OTRIVI/ TITOTT								
BillType Data								
Code	Denomination							
1	Reserved							
2	5							
3	10							
4	20							
5	50							
6	100							
7	200							

Appendix

XBA-AED6 (Pulse/JPSTD/ccTalk/ICT104U/RS232 A0)

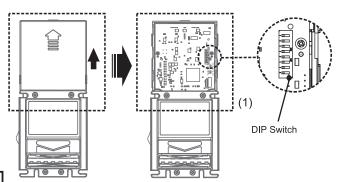
	FUNCTION		SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Pulse Mode	Other Mode								
*	Inhibit Active HIGH	Table.1	ON							
	Inhibit Active LOW	Table.1	OFF							
	Reserved	Reserved		ON						
*	Reserved	Reserved		OFF						
*	50ms LO / 50ms HI				OFF	OFF				
	50ms LO / 100ms HI				OFF	ON				
	65ms LO / 75ms HI				ON	OFF				
	100ms LO / 150ms HI				ON	ON				
*	Credit-Pulse Normal HIGH	ccTalk 16-bit Encryption mode & Restore Mode (See Table 1)					ON			
	Credit-Pulse Normal LOW	ccTalk 16-bit Decrypt mode & 8-bit checksum.(See Table.1)					OFF			
*	Pulse Mode	Pulse Mode						ON		
	Other Mode	Other Mode						OFF		
*	1 pulse / AED 5	ccTalk Mode							OFF	OFF
	2 pulses / AED 5	RS232 A0 Mode							OFF	ON
	4 pulses / AED 5	ICT104U Mode							ON	OFF
	20 pulses / AED 5	JPSTD Mode							ON	ON

★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.

Table 1

	SW1	SW5
8-Bit Checksum	OFF	OFF
Restore mode(Note.1)	OFF	ON
CRC 16-Bit Decrypt Checksum	ON	OFF
CRC 16-Bit Encryption Checksum	ON	ON



Affer setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note: 1.Turn this dip to with restart the bill acceptor will restore.

2.CCTalk address to its default value of 40 and the encryption key to its default value of 123456.

3.Calibration card is needed.

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