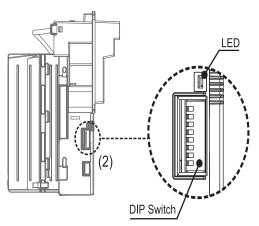
XBA-KZT6 Option Switch Settings

Supported bill KZT 200, 500, 1000, 2000, 5000, 10000 6bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject KZT 200	ON							
←	Accept KZT 200	OFF							
	Reject KZT 500 & 1000		ON						
*	Accept KZT 500 & 1000		OFF						
	Reject KZT 2000			ON					
*	Accept KZT 2000			OFF					
	Reject KZT 5000				ON				
*	Accept KZT 5000				OFF				
	Reject KZT 10000					ON			
*	Accept KZT 10000					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up				·				
*	Reject Banknote when Power-up						OFF		
	Reserved								ON
+	Reserved							OFF	

This dip switch is located at the side of XBA.



Currency Assign Data

ı	Interface Bill value	Pulse	JPSTD	ccTalk	ICT104U	RS232 A0	
ı	BV1	KZT 200	KZT 200	KZT 200	KZT 200	KZT 200	
ı	BV2	KZT 500		KZT 500	KZT 500	KZT 500	
ı	BV3	KZT 1000		KZT 1000	KZT 1000	KZT 1000	
ı	BV4	KZT 2000		KZT 2000	KZT 2000	KZT 2000	
ı	BV5	KZT 5000		KZT 5000	KZT 5000	KZT 5000	
ı	BV6	KZT10000		KZT10000	KZT10000	KZT10000	

RS232 A0 INFORMATION

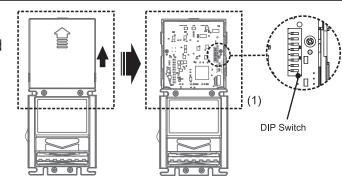
BillType Data						
Code	Denomination					
1	Reserved					
2	KZT 200					
3	KZT 500					
4	KZT 1000					
5	KZT 2000					
6	KZT 5000					
7	KZT 10000					

Appendix

XBA-KZT6 (Pulse/JPSTD/ICT104U/RS232 A0/ccTalk)

	FUNCTION		SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Pulse Mode	Other Mode								
	Inhibit Active HIGH		ON							
*	Inhibit Active LOW		OFF							
	Address 41 (ccTalk only)			ON						
*	Address 40 (ccTalk only)			OFF						
*	50ms LO / 50ms HI		•		OFF	OFF				
	50ms LO / 100ms HI				OFF	ON				
	65ms LO / 75ms HI				ON	OFF				
	100ms LO / 150ms HI				ON	ON				
*	Credit-Pulse Normal HIGH	ccTalk 16-bit Encryption mode & Restoe Mode.(See Table.1)					ON			
	Credit-Pulse Normal LOW	ccTalk 16-bit Decrypt mode & 8-bit checksum(See Table.1)					OFF			
*	Pulse Mode			•			ON			
	Other Mode							OFF		
*	1 pulse / KZT 100	ccTalk Mode				OFF	OFF			
	2 pulses / KZT 100	RS232 A0 Mode)			OFF	ON			
	5 pulses / KZT 100	ICT104U Mode							ON	OFF
	20 pulses / KZT 100	JPSTD Mode							ON	ON

This dip switch is located on the CPU board, remove the CPU board cover first.



★ Manufacture setting

Affer setting dip switch of the credit pulses, you should reset the bill acceptor again. Note: 1.Turn this dip to with restart the bill acceptor will restore.

- CCTalk address to its default value of 40 and the encryption key to its default value of 123456.
- 3.Calibration card is needed.

Table 1

	SW2 DIP 1	OFF	OFF	ON	★ ON
ı	SW2 DIP 5	OFF	ON	OFF	★ ON
		8-Bit Checksum	Restore mode (Note.2)	CRC 16-Bit Decrypt Checksum	CRC 16-Bit Encryption Checksum

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