

TAO-A.V-MYR4

TAO-A.V Option Switch Settings: 1(Pulse/ICT104V/ICT104U/MDB)

Supported bill M\$ 1, 5, 10, 20

4 bills.

TAO-A.V dip-switch settings and functions:

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	SW1	SW2	SW3	SW4	
Reject M\$ 1	ON												
★ Accept M\$ 1	OFF												
Reject M\$ 5		ON											
★ Accept M\$ 5		OFF											
Reject M\$ 10			ON										
★ Accept M\$ 10			OFF										
Reject M\$ 20				ON									
★ Accept M\$ 20				OFF									
High Acceptance					ON								
★ High Security					OFF								
Stack Banknote when Power-up						ON							
★ Reject Banknote when Power-up						OFF							
Pulse Mode	Other Mode												
Harness Disable	BA stops working when fishing							ON					
★ Harness Enable	Normal							OFF					
Inhibit Active High								ON					
★ Inhibit Active Low								OFF					
★ 1 pulse / M\$ 1								OFF	OFF				
2 pulses / M\$ 1								ON	OFF				
4 pulses / M\$ 1								OFF	ON				
20 pulses / M\$ 1								ON	ON				
★ Interface Timing Conversion	50ms LO / 50ms HI										OFF	OFF	
	60ms LO / 300ms HI										ON	OFF	
	30ms LO / 50ms HI										OFF	ON	
	150ms LO / 150ms HI										ON	ON	

★ Manufacture setting

◆ Note : (1) Default setting.

(2) Use 2nd and 3rd dip of the internal dip-switch to change different interface protocol.

(3) Remove the front cover and the dip-switch in on the CPU board.

(Please refer to "SW1" setting.)

BV1=M\$1, BV3=M\$5, BV4=M\$10, BV5=M\$20

(4) High acceptance mode will increasing accepting rate, however, it will reduce the security level of Bill Acceptor.

Appendix

TAO-A.V-MYR4(Pulse/ICT104V/ICT104U/MDB)

Interface Settings: 3(Pulse) Currency Assign Data

INTERFACE	SW1	SW2	SW3	SW4
★ Credit-Pulse Normal HIGH	ON			
Credit-Pulse Normal LOW	OFF			
★ Pulse Mode		ON	OFF	
ICT104U Mode		OFF	OFF	
ICT104V Mode		OFF	ON	
MDB Interface		ON	ON	
★ Reserved				ON
				OFF

Interface Bill value	ICT	Pulse	MDB
BV1	M\$ 1	M\$ 1	M\$ 1
BV2			
BV3	M\$ 5	M\$ 5	M\$ 5
BV4	M\$ 10	M\$ 10	M\$ 10
BV5	M\$ 20	M\$ 20	M\$ 20

★ Manufacture setting

Interface Settings: 3(ICT104U)

Interface Settings: 3(ICT104V)

INTERFACE	SW1	SW2	SW3	SW4
★ Reserved	ON			
Reserved	OFF			
Pulse Mode		ON	OFF	
★ ICT104U Mode		OFF	OFF	
ICT104V Mode		OFF	ON	
MDB Interface		ON	ON	
★ Reserved				ON
				OFF

INTERFACE	SW1	SW2	SW3	SW4
★ Reserved	ON			
Reserved	OFF			
Pulse Mode		ON	OFF	
★ ICT104U Mode		OFF	OFF	
ICT104V Mode		OFF	ON	
MDB Interface		ON	ON	
★ Reserved				ON
				OFF

★ Manufacture setting

★ Manufacture setting

TAO-V-MYR4

Interface Settings: 3(MDB)

FUNCTION	SW1	SW2	SW3	SW4
★ Scaling Factor (SF) = 100 Decimal Point Position (DPP) = 2	ON			
Scaling Factor (SF) = 1 Decimal Point Position (DPP) = 0	OFF			
Pulse Mode		ON	OFF	
ICT104U Mode		OFF	OFF	
ICT104V Mode		OFF	ON	
★ MDB Interface		ON	ON	
MDB Special Mode(No Error Code)(MDB Only)				ON
★ MDB Normal Mode				OFF

★ Manufacture setting